



Patent
Attorney Dkt. No. CA7038322001
Serial No. 10/029,596

DECLARATION UNDER 35 CFR 1.132

I, Chung-Yang Huang, hereby declare that I conceived the subject matter disclosed and claimed in the pending patent application entitled Solving Constraint Satisfiability Problem for Automatic Generation of Design Verification Vectors, filed on October 24, 2001, with Serial No. 10/029,596.

I further declare that I am a co-author of the paper entitled "Assertion Checking by Combined Word-level ATPG and Modular Arithmetic Constraint-Solving Techniques." Insofar as that paper contains subject matter disclosed or claimed in the above-identified patent application, I hereby declare I am the originator of that subject matter, notwithstanding the authorship of the paper.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under 18 U.S.C. 1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

11/07/2005

Date

Chung-Yang Huang
Chung-Yang Huang